## **SPECIFICATION AMENDMENTS**

Please replace paragraph [0018] with the following rewritten paragraph:

[0018] As was explained above with earlier reference to Figure 1, during the continued operation of the newly active memory pool 30A, the newly inactive memory pool 20A is updated at intervals with the contents of the newly active memory pool 30A. Following updating, the contents of the newly inactive memory pool 20A, comprising the connection information 22d, will match those of the newly active memory pool 30A as shown in Figure 3. Each of the connections associated with the connection information 24d that is respectively found at the storage locations of the newly inactive memory 20A labeled a', b', c' and d' will therefore correspond to each of the connections associated with the connection information 24c that is respectively found at the storage locations of the newly active memory 30A labeled u', v', w' and x'. Likewise, each of the connections associated with the connection information 26d that is respectively found at the storage locations of the newly inactive memory 20A labeled e' and [[f]] f'will therefore correspond to each of the connections associated with the connection information 26c that is respectively found at the storage locations of the newly active memory 30A labeled y' and z'. This correspondence is once again maintained in the cross-reference table 40 or the like, which maps the connections 1 to 6 in relation to the storage locations u' to z' of the connection information of the newly active memory 30A. Furthermore, the cross-reference table 40 in turn maps the storage locations u' to z' of the connection information of the newly active memory 30A to the corresponding storage locations a' to f of the connection information of the newly inactive memory 20A. The process as explained above is then repeated for so long as the memory architecture 10 is being operated in a redundant manner.